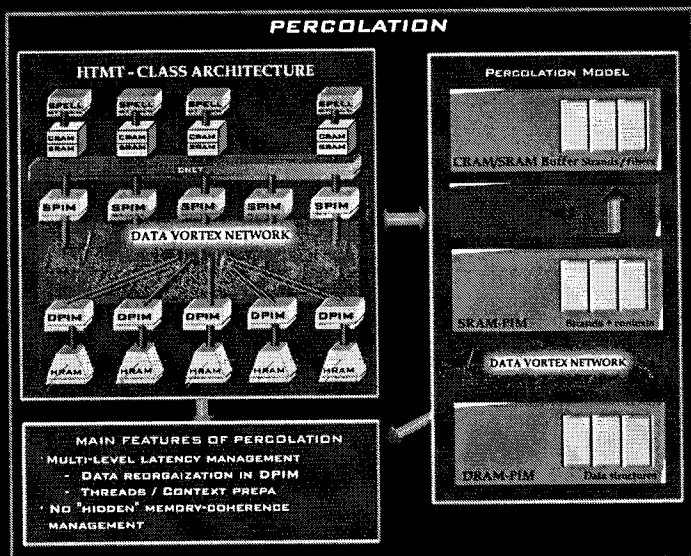
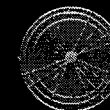
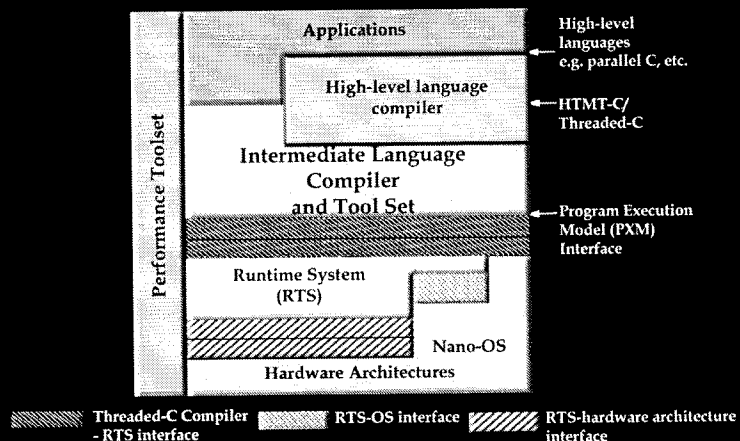


HTMT

HTMT PROGRAM EXECUTION MODEL



INITIAL ROADMAP FOR A HPC SYSTEM SOFTWARE ARCHITECTURE



Note:

- The threaded-C compiler has part of its functions embedded in RTS
- The RTS will work with architecture and OS layers to provide the PXM interface
- The performance models are defined across all layers

PERFORMANCE EVALUATION THROUGH EMULATION / SIMULATION OF IMPORTANT BENCHMARK APPLICATIONS

- Definition of THREADED-C 1.0 (CAPSL-TM19 3/1998) and THREADED-C 2.0 (CAPSL-TM39, 6/2000)
- The RTS will work with architecture and OS layers to provide the PXM interface
- A Draft Definition of HTMT THREADED-C 1.0 (3/2000)
- Design and Implementation of the HTMT Emulation Testbed
- Implementation and Performance Evaluation of Selected Benchmark Programs

